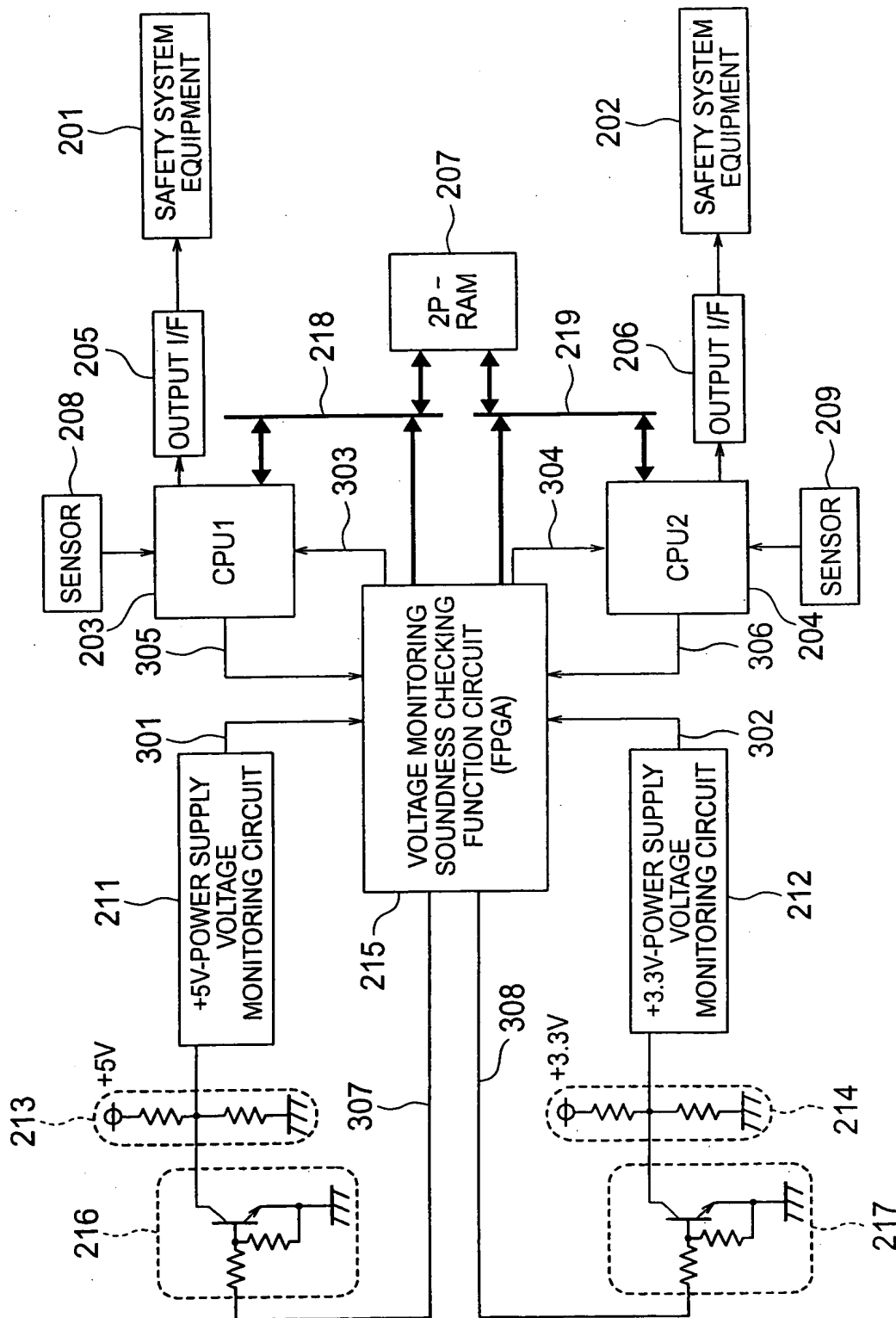




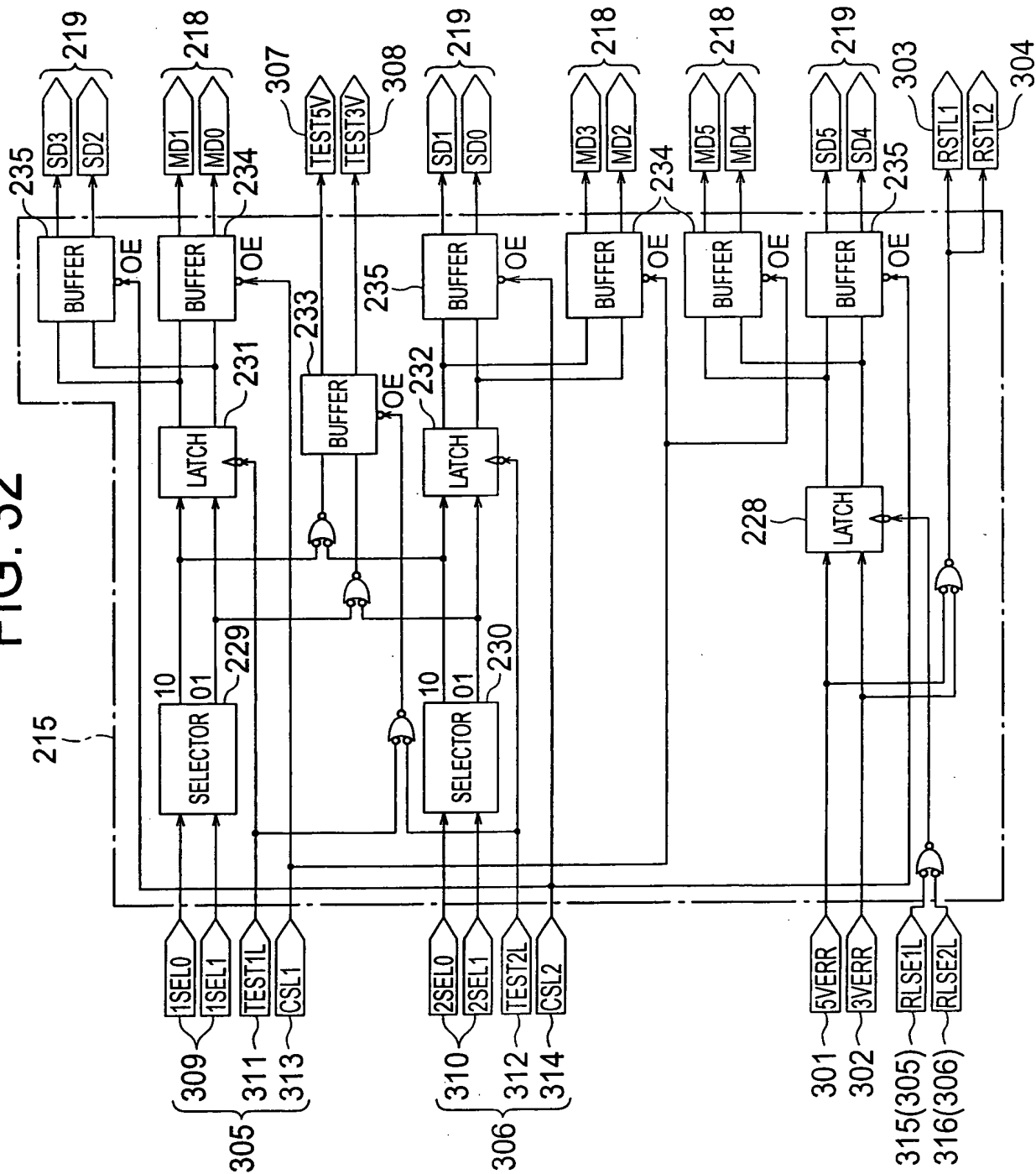
FIG. 31





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 Inventor: Akihiro CHIDA  
 Serial No: 10/572,351  
 Prior to a first examination on the merits  
 Replacement Sheet

FIG. 32





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FIG. 33

DATA BIT	MD5 (SD5)	MD4 (SD4)	MD3 (SD3)	MD2 (SD2)	MD1 (SD1)	MD0 (SD0)
MEANING OF VALUE	<ABNORMAL VOLTAGE CRITICAL VALUE>		<SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON THE OTHER SIDE)>		<SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON OWN SIDE)>	
	MD5(SD5) : 0→3.3V ABNORMAL VOLTAGE 1→NORMAL VOLTAGE		MD3 (SD3)   MD2 (SD2)		MD1 (SD1)   MD0 (SD0)	
	MD4 (SD4) : 0→5V ABNORMAL VOLTAGE 1→NORMAL VOLTAGE		0   1 1   0 OTHER THAN ABOVE-MENTIONED COMBINATION		0   1 1   0 OTHER THAN ABOVE-MENTIONED COMBINATION	
				SELECTED VOLTAGE		SELECTED VOLTAGE
				3.3V 5V NOT SELECTED		3.3V 5V NOT SELECTED